

Computer Memory Test Labs Intel® Approved Final Test Report

MOTHERBOARD I	MANUFACTURER	CHIPSET	TEST #	STATUS
NUC5CPYH	Intel	Intel® Braswell	027149	PASSED

MEMORY MODULE INFORMATION

CMTL Part #: ITC006

Manufacturer Name: Insignis Technology Corporation, Inc.

Manufacturer Part #: NMLS0220-8KET

DRAM Manufacturer: INSIGNIS Environment Code: RoHS-Halogen Free

DRAM Part #: NDL28PFH-8KIT Low Profile: 30mm LP

Module Config: 256M x 64 Cas Latency: 11

Module Size: **2GB**

PCB Part #: KO-8248 Bank: 2

PCB Layer Count: 6 Layer Assembly Type: JEDEC Standard/Non-stacked DRAM

Module Information: Unbuffered 1.35V Non-ECC 1600MHz DDR3 SODIMM

SYSTEM INFORMATION		TESTING DETAILS		
MINIMUM SYSTEM II Minimum System:	NFORMATION NUC5CPYH	35°C~55°C Temperat	ure / Standard Voltage	
Serial Number: Board Revision:	G6PY526001EC H74562-402	Start Date:	6/10/2017	
Bios Memory Count:	2GB	Stop Date:	6/11/2017	
Processor Code: Bios at time of test:	Intel® Celeron® N3050 @ 1.60GHz PYBSWCEL.86A.0058.2016.1102.1842	Insertion Test:	Pass	
RST PRO Count:	1G 960M	SPD Check:	Pass	
MAXIMUM SYSTEM INFORMATION		Minimum Load:	Pass	
Maximum System: Serial Number:	NUC5CPYH G6PY526001EV	Maximum Load:	Pass	
Board Revision:	H74562-402 2GB	S0 ~ S5 Test:	Pass	
Bios Memory Count: Processor Code: Bios at time of test:	Processor Code: Intel® Celeron® N3050 @ 1.60GHz	Power Cycle Test:	Pass	
R.S.T PRO Count:	1G 960M	Test Card Version:	R.S.T Premium v8.50	

Microsoft Windows-7 64bit version is used as it support over 4GB memory and must support target chipset/CPU platform. All board drivers must be loaded when drivers are available on Intel web site. Front panel power cycling test, ACPI S3 sleep state cycling test, and Hibernate S4/S5 state cycling test is done up to 20 cycle each when the board support it. At least 2 loops of all test under R.S.T PRO3 to be completed.

Test Notes:

Max configuration, 5 Loops Passed, 0 Error; Memory speed running @ 1600MHz This NUC Supports Intel® Braswell Chipset

UUT under test reached +50°C