



# Computer Memory Test Labs

## Intel® Approved Final Test Report

MOTHERBOARD	MANUFACTURER	CHIPSET	TEST #	STATUS
NUC5i7RYH	Intel	Intel® Broadwell	027126	PASSED

### MEMORY MODULE INFORMATION

**CMTL Part #: ITC002**

Manufacturer Name: **Insignis Technology Corporation, Inc.**

Manufacturer Part #: **NMLS0110-8KIT (i-Temp)**

DRAM Manufacturer: **INSIGNIS**

Environment Code: **RoHS-Halogen Free**

DRAM Part #: **NDL18PFH-8KIT**

Low Profile: **30mm LP**

Module Config: **128M x 64**

Cas Latency: **11**

Module Size: **1GB**

Bank: **1**

PCB Part #: **KOD38SRCB V0.55**

PCB Layer Count: **6 Layer**    Assembly Type: **JEDEC Standard/Non-stacked DRAM**

Module Information: **Unbuffered 1.35V    Non-ECC    1600MHz    DDR3 SODIMM**

SYSTEM INFORMATION	TESTING DETAILS
<p><b><u>MINIMUM SYSTEM INFORMATION</u></b></p> <p>Minimum System: NUC5i7RYH            Serial Number: G6RY512001QX            Board Revision: H73774-100            Bios Memory Count: 1GB            Processor Code: Intel® Core™ i7-5557U @ 3.10GHz            Bios at time of test: RYBDWi35.86A.0353            RST PRO Count: 928M</p> <p><b><u>MAXIMUM SYSTEM INFORMATION</u></b></p> <p>Maximum System: NUC5i7RYH            Serial Number: G6RY512001QZ            Board Revision: H73774-100            Bios Memory Count: 2GB            Processor Code: Intel® Core™ i7-5557U @ 3.10GHz            Bios at time of test: RYBDWi35.86A.0353            R.S.T PRO Count: 1G 928M</p>	<p><b>35°C~55°C Temperature / Standard Voltage</b></p> <p>Start Date: 6/5/2017            Stop Date: 6/6/2017</p> <p>Insertion Test: Pass            SPD Check: Pass            Minimum Load: Pass            Maximum Load: Pass            S0 ~ S5 Test: Pass            Power Cycle Test: Pass</p> <p>Test Card Version: R.S.T Premium v8.50</p>

Microsoft Windows-7 64bit version is used as it support over 4GB memory and must support target chipset/CPU platform. All board drivers must be loaded when drivers are available on Intel web site. Front panel power cycling test, ACPI S3 sleep state cycling test, and Hibernate S4/S5 state cycling test is done up to 20 cycle each when the board support it. At least 2 loops of all test under R.S.T PRO3 to be completed.

#### Test Notes:

Min configuration, 5 Loops Passed, 0 Error; Memory speed running @ 1600MHz

Max configuration, 5 Loops Passed, 0 Error; Memory speed running @ 1600MHz

This NUC Supports Intel® Braswell Chipset

UUT under test reached +50°C

This memory part was tested and Industrial certified at -40°C & +85°C without any error